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1 IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

2 Application Serial No. 09/961,114
3 Filing Date 09/20/2001
4 Inventor Perino et al.
5 Applicant Rambus, Inc.
6 Group Art Unit 2833
7 Examiner
8 Attorney's Docket No. RB1-035USC3
9 Title: Chip Socket Assembly and Chip File Assembly for Semiconductor Chips

10 **SUBSTITUTE DRAWING REQUEST**

11 To: Drawing Review Branch
12 Commissioner of Patents and Trademarks,
13 Washington, D.C. 20231

14 From: Daniel L. Hayes (Tel. 509-324-9256; Fax 509-323-8979)
15 **Customer No. 29150**



29150

PATENT & TRADEMARK OFFICE

16 Applicant submits herewith a new set of formal drawings to correct
17 informalities noted by the official draftsperson in Notice to File Corrected
18 Application Papers.

19 Respectfully Submitted,

20 Date: 12/10/01

21 By: Daniel L. Hayes
22 Daniel L. Hayes
23 Reg. No. 34,618
24 (509) 324-9256